

Samsung Confidential
Product Information

DATE: 29. Nov. 2011

SAMSUNG TFT-LCD

MODEL: LTI400HA07

The Information Described in this Specification is Preliminary and can be changed without prior notice

| APPROVED BY | DATE | PREPARED BY | DATE | |
|--------------|--------------|----------------|--------------|--|
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* Revision History

| Date | Rev. No | Page | Summary |
|---------------------|------------|------|--------------|
| 29, Nov, 2011 | 000 | all | First issued |

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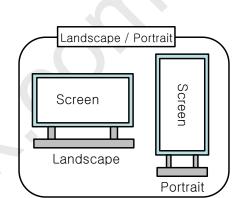
General Description

Description

LTI400HA07 is a color active matrix liquid crystal display (LCD) that uses amorphous silicon TFT(Thin Film Transistor) as switching components. This model is composed of a TFT LCD panel, a driver circuit and a back light unit. The resolution of a 40.0" is 1920 x 1080 and this model can display up to 16.7 million colors with wide viewing angle of 89° or higher in all directions. This panel is intended to support applications to provide a excellent performance for Flat Panel Display such as Home-alone Multimedia TFT-LCD TV, Display terminals for AV application products, and Digital Information Display (DID).

Features

- RoHS compliance (Pb-free)
- High contrast ratio, High aperture ratio
- SPVA(Super Patterned Vertical Align) mode
- Wide viewing angle (±178°)
- High speed response
- Landscape / Portrait type compatible
- Wide UXGA (1920 x 1080 pixels) resolution (16:9)
- Low power consumption
- Direct Type 12 CCFTs(Cold Cathode Fluorescent Tube)
- DE(Data Enable) mode
- LVDS (Low Voltage Differential Signaling) interface (2pixel/clock)



General Information

| Items | Specification | Unit | Note |
|---------------------|---|-------------------|--------|
| Module Size | 952.0(W _{TYP}) x 551.0(H _{TYP}) | mm | ±1.0mm |
| Module Size | 56.6(D _{MAX}) | | |
| Weight | 10,000(Max.) | g | |
| Pixel Pitch | 0.46125(H) x 0.46125(V) | mm | |
| Active Display Area | 885.6(H) x 498.15(V) | mm | |
| Surface Treatment | Haze 44% , Hard-coating (3H) | | |
| Display Colors | 8 bit - 16.7M | colors | |
| Number of Pixels | 1920 x 1080 | pixel | |
| Pixel Arrangement | RGB vertical stripe | | |
| Display Mode | Normally Black | | |
| Luminance of White | 450 (Typ.) | cd/m ² | |

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1. Absolute Maximum Ratings

If the condition exceeds maximum ratings, it can cause malfunction or unrecoverable damage to the device.

| Iten | Symbol | Min. | Max. | Unit | Note | |
|---|---------------|---------------------|------|------|------|---------|
| Power Supp | V_{DD} | GND-0.3 | 13.2 | V | (1) | |
| Storage temperature | | T _{STG} | -20 | 65 | C | (2) |
| Glass surface temperature (Operation) | Center | T _{CENTER} | 0 | 50 | °C | (2),(5) |
| Shock (non - operating) | | S _{nop} | - | 50 | G | (3) |
| Vibration (non | - operating) | V_{nop} | - | 1.5 | G | (4) |

Note (1) Ta= 25 \pm 2 °C

- (2) Temperature and relative humidity range are shown in the figure below.
 - a. 90 % RH Max. (Ta ≤ 39 °C)
 - b. Relative Humidity is 90% or less. (Ta > 39 °C)
 - c. No condensation
- (3) 11ms, sine wave, one time for $\pm X$, $\pm Y$, $\pm Z$ axis
- (4) 10-300 Hz, Sweep rate 10min, 30min for X,Y,Z axis

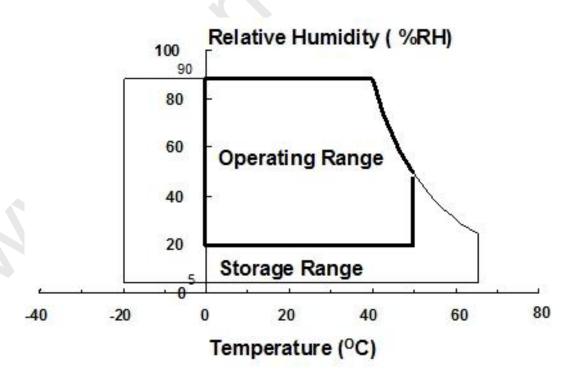
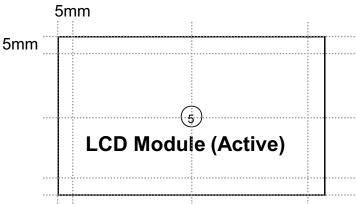


Fig. Temperature and Relative humidity range

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|---|
|---|



(5) Definition of test point



T_{CENTER}: Temperature of the center of the glass surface (Test point 5)

2. Application information for DID (Digital Information Display)

A long-term display like DID application may cause uneven display including image retention. To optimize module's lifetime and function, several operating usages are required.

- 1. Normal operating condition
 - Temperature: 20 ± 15 °C
 - Humidity: 65 \pm 20 %
- Display pattern: moving picture or regular switchover display

Note) Long-term static information image may cause uneven display.

- 2. Operating usages under abnormal operating condition. Note (1)
 - a. Ambient condition
 - Well-ventilated place is recommended to set up DID system.
- b. Power off and screen saver
- Periodical power-off or screen saver is needed after long-term static display. Note (2)
- 3. Operating usages to protect uneven display due to long-term static information display
 - a. Suitable operating time for B-DID: under 12 hours a day.
 - b. Periodical display contents change from static image to moving picture.
 - Liquid crystal refresh time is required.
- c. Periodical background color and character (image) color change
- Use different colors for background and character (image), respectively.
- Change colors periodically.
- d. Avoid combination of background and character with large different luminance.

Note (1) Abnormal condition means every operating condition except normal operating condition.

- Note (2) Moving picture or black pattern is strongly recommended for screen saver.
- 4. Lifetime in this spec is guaranteed only when DID is used under right operating usages.

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3. Optical Characteristics

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The optical characteristics should be measured in a dark room or equivalent. Measuring equipment: TOPCON BM-7, SPECTRORADIOMETER SR-3

 $(Ta = 25 \pm 2^{\circ}C, VDD = 12V, fv = 60Hz, f_{DCLK} = 148.5MHz, I_{L} = 14mArms)$

| | | (| LO <u>-</u> L O, VD | J v, . | v 00112, | DCLK | 0.01111 12, 1 | L ' '''' '''' |
|-------------------------------------|---------|------------------|---------------------|--------|----------|-------|-------------------|---------------|
| Item | | Symbol | Condition | Min. | Тур. | Max. | Unit | Note |
| Contrast F (Center of s | | C/R | | 2000 | 3000 | - | | (3) SR-3 |
| Response Time | G-to-G | Tg | | - | 8 | - | msec | (5) BM-7 |
| Luminance of (Center of s | | Y _L | Normal | 350 | 450 | - | cd/m ² | (6) SR-3 |
| | Red | Rx | θ L,R =0 | | 0.637 | | | |
| | Reu | Ry | θ U,D =0 | | 0.331 | | | |
| | Green | Gx | Viewing Angle | | 0.292 | | | |
| Color Chromaticity (CIE 1931) | Orccii | Gy | | TYP. | 0.605 | TYP. | | (7),(8) |
| | Blue | Bx | | -0.03 | 0.148 | +0.03 | | SR-3 |
| | blue | Ву | | | 0.061 | | | |
| | White | Wx | | 0.2 | 0.280 | | | |
| | vviiite | Wy | | | 0.290 | | | |
| Color Ga | mut | - | | | 72 | - | % | (7) SR-3 |
| Color Temp | erature | - | | - | 10,000 | - | К | (7) SR-3 |
| | Han | θ_{L} | | 79 | 89 | - | | |
| Viewing Angle | Hor. | θ_{R} | C/D>10 | 79 | 89 | - | Dogras | (8) |
| | Ver. | $\theta_{\sf U}$ | C/R≥10 | 79 | 89 | - | Degree | ΕŹ |
| | ver. | θ_{D} | | 79 | 89 | - | | |
| Brightness Ui (9 Poin | | B _{uni} | | - | - | 25 | % | (4) SR-3 |

Note (1) Test Equipment Setup

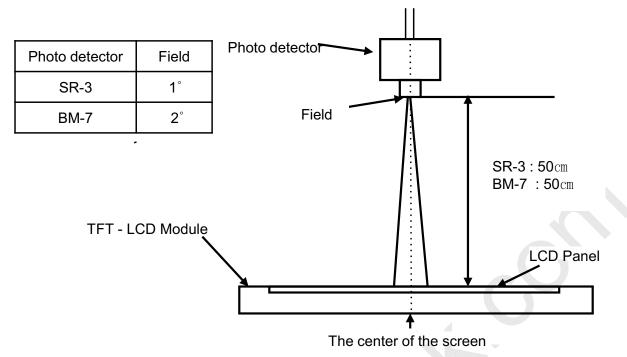
The measurement should be executed in a stable, windless and dark room between 40min and 60min after lighting the backlight at the given temperature for stabilization of the backlight. This should be measured in the center of screen.

Single lamp current: 14mA

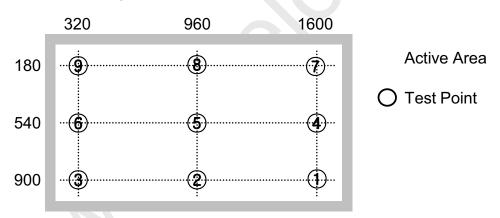
Environment condition : Ta = 25 ± 2 °C

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Note (2) Definition of test point



Note (3) Definition of Contrast Ratio (C/R)

: Ratio of gray max (Gmax) & gray min (Gmin) at the center point ⑤ of the panel

$$C/R = \frac{G \max}{G \min}$$

Gmax: Luminance with all pixels white Gmin: Luminance with all pixels black

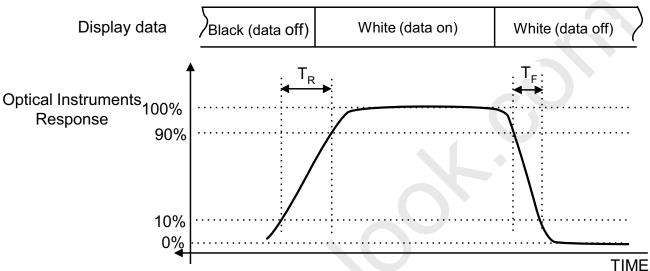
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|---|-------|

Note (4) Definition of 9 points brightness uniformity

$$Buni = 100* \frac{(B \max - B \min)}{B \max}$$

Bmax : Maximum brightness Bmin : Minimum brightness

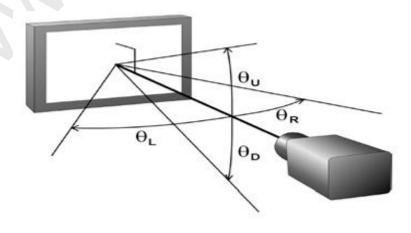
Note (5) Definition of Response time : Sum of Tr, Tf



Note (6) Definition of Luminance of White: Luminance of white at center point ⑤

Note (7) Definition of Color Chromaticity (CIE 1931)
Color coordinate of Red, Green, Blue & White at center point ⑤

Note (8) Definition of Viewing Angle : Viewing angle range (C/R ≥ 10)



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4. Electrical Characteristics

4.1 TFT LCD Module

The connector for display data & timing signal should be connected.

Ta = 25° C \pm 2 $^{\circ}$ C

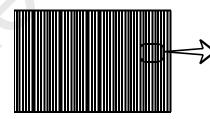
| Item | | Symbol | Min. | Тур. | Max. | Unit | Note |
|-------------------------|---------------|-------------------|-------|-------|-------|------|---------|
| Voltage of Power Supply | | V_{DD} | 10.8 | 12.0 | 13.2 | V | (1) |
| Current | (a) Black | | - | 720 | - | mA | |
| of Power | (b) White | I _{DD} | - | 1350 | - | mA | (2),(3) |
| Supply | (c) N-Pattern | | - | 1290 | - | mA | þ |
| Vsync Frequency | | f_{\vee} | 48.0 | 60.0 | 62.0 | Hz | |
| Hsync Frequency | | f _H | 50.0 | 67.5 | 75.0 | kHz | |
| Main Frequency | | f _{DCLK} | 130.0 | 148.5 | 155.0 | MHz | |
| Rush Curr | ent | I _{RUSH} | - | - 1 | 3 | Α | (4) |

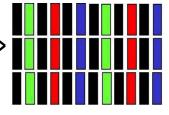
Note (1) The ripple voltage should be controlled under 10% of V_{DD} .

- (2) fv = 60Hz, fdclk = 148.5MHz, V_{DD} = 12.0V, DC Current.
- (3) Power dissipation check pattern (LCD Module only)
- a) Black Pattern
- b) White Pattern
- c) N-Pattern

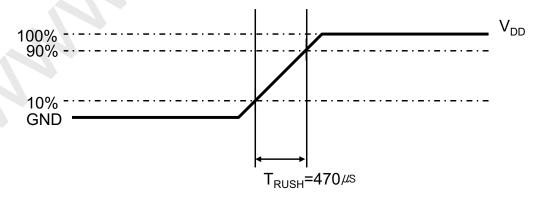








(4) Measurement Conditions



Rush Current I_{RUSH} can be measured when T_{RUSH} is 470 μ s.

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4.2 Back Light Unit

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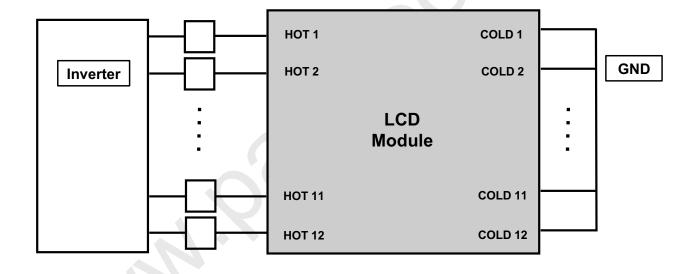
The back light unit contains 12 direct-lighting type CCFTs (Cold Cathode Fluorescent Tube). The characteristics of lamps are shown in the following tables.

Ta=25 \pm 2°C

| Item | Symbol | Min. | Тур. | Max. | Unit | Note |
|---------------------|---------|--------|------|------|-------|------|
| Lamp Current | ال | 8.0 | 14.0 | 16.0 | mArms | |
| Lamp Voltage | V_{L} | 825 | 855 | 955 | Vrms | > |
| Operating Life Time | Hr | 50,000 | - | - | Hour | (1) |

Note (1) It is defined as the time to take until the brightness reduces to 50% of its original value.

[Operating condition : Ta = $25\pm2^{\circ}$ C, IL = 14.0mArms, For single lamp only]



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4.3 Inverter Input Condition & Specification

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| Itomo | Cymbal | Conditions | Sp | pecificatio | ns | Lloit | Noto |
|---------------------|--------------------|------------------------|------|-------------|------|-------|-------------------------|
| Items | Symbol | Conditions | Min. | Min. Typ. I | | Unit | Note |
| Input Voltage | Vin | - | 22 | 24 | 26 | V | Ta=25 ±2 °C |
| Input Current | lin | Vin=24.0V Vdim=3.3V | - | 5.32 | 5.68 | A | 5 |
| Lamp Current | I _{O,MAX} | Vdim=3.3V | 13.3 | 14.0 | 14.7 | mArms | After 1 hour Warm-up |
| Frequency | F _{LAMP} | Vin=24.0V Vdim=3.3V | 46 | 48 | 50 | kHz | |
| Backlight | ON | \/in=24.0\/ | 2.4 | - | 5.25 | V | |
| On/Off | OFF | Vin=24.0V | 0 | - | 0.8 | V | - |
| Internal Dimming | - | Vin=24.0V | 0 | 0 | 3.3 | V | |

Note (1) Power Consumption is measured at 450[cd/m2] of luminance condition which is the typical luminance value. Lamp Current is measured at the point before Lamp.

| | | | | _ | |
|-------|------------|---------|-----------------|------|---------|
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5. Input Terminal Pin Assignment

5.1 Input Signal & Power

Connector: FI-RE51S-HF (JAE)

| PIN No. | Descr | ription | PIN No. | Descr | iption | | |
|---------|----------------|---------|---------|--------|---------------|--|--|
| 1 | Vdd (| (12V) | 26 | | RE[0]P | | |
| 2 | Vdd (| (12V) | 27 | | RE[1]N | | |
| 3 | Vdd (| (12V) | 28 | | RE[1]P | | |
| 4 | Vdd (| (12V) | 29 | | RE[2]N | | |
| 5 | Vdd (| (12V) | 30 | Even | RE[2]P | | |
| 6 | No Con | nection | 31 | LVDS | GND | | |
| 7 | GN | ND | 32 | Signal | RECLK- | | |
| 8 | GN | ND | 33 | | RECLK+ | | |
| 9 | GN | ND | 34 | • | GND | | |
| 10 | | RO[0]N | 35 | 7 | RE[3]N | | |
| 11 | 1 | RO[0]P | 36 | | RE[3]P | | |
| 12 | 1 | RO[1]N | 37 | No Con | nection | | |
| 13 | | RO[1]P | 38 | No Con | No Connection | | |
| 14 | 1 | RO[2]N | 39 | GN | ND | | |
| 15 | Odd | RO[2]P | 40 | No Con | nection | | |
| 16 | LVDS Signal | GND | 41 | No Con | nection | | |
| 17 | | ROCLK- | 42 | No Con | nection | | |
| 18 | | ROCLK+ | 43 | No Con | nection | | |
| 19 | | GND | 44 | No Con | nection | | |
| 20 | 1 . 7 | RO[3]N | 45 | LVDS | Option | | |
| 21 | | RO[3]P | 46 | No Con | nection | | |
| 22 | No Con | nection | 47 | No Con | nection | | |
| 23 | No Con | nection | 48 | No Con | nection | | |
| 24 | GN | ND | 49 | No Con | nection | | |
| 25 | Even LVDS | RE[0]N | 50 | No Con | nection | | |
| | | | 51 | No Con | nection | | |

Note(1) No Connection: These pins are only used for SAMSUNG internal purpose.

(2) LVDS Option : High (3.3 V) → Normal LVDS format

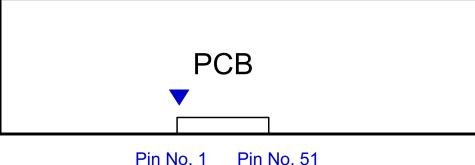
: Low (GND) or Open (N.C) \rightarrow JEIDA LVDS format

Sequence :On = $VDD(T1) \ge LVDS$ Option $\ge Interface Signal(T2)$

Off = Interface Signal(T3) ≥ LVDS Option ≥ VDD

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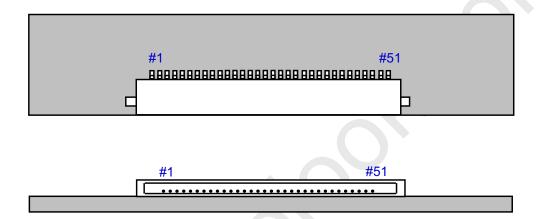


Fig. Connector diagram

- a. All GND pins should be connected together and also be connected to the LCD's metal chassis.
- b. All power input pins should be connected together.
- c. All N.C pins should be separated from other signal or power.

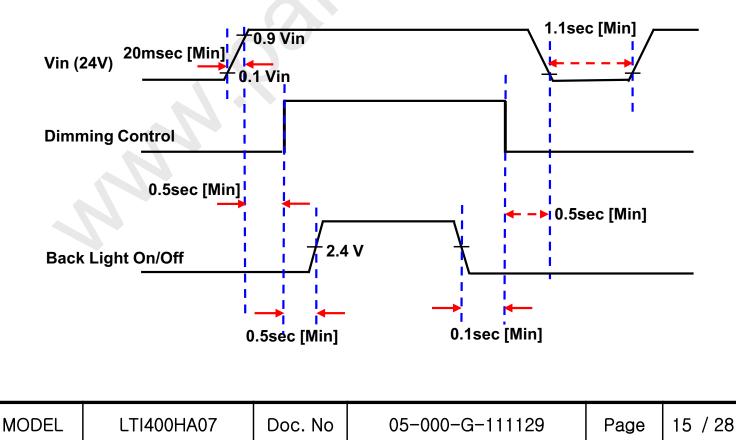
5.2 Inverter Input Pin Configuration

Connector: YEON HO, 20022WR-14B1

| Pin No. | Pin Configuration(FUNCTION) |
|---------|--|
| 1 | Vin (24V) |
| 2 | Vin (24V) |
| 3 | Vin (24V) |
| 4 | Vin (24V) |
| 5 | Vin (24V) |
| 6 | GND |
| 7 | GND |
| 8 | GND |
| 9 | GND |
| 10 | GND |
| 11 | No connection |
| 12 | Backlight On /Off [On: 2.4 ~ 5.25V, Off: 0 ~ 0.8V] |
| 13 | INT DIM :INTERNAL DIMMING SIGNAL (0V: Min 3.3V: Max) |
| 14 | No connection |

Note (5) LVDS Connector

5.3 Inverter Input Power Sequence



5.4 LVDS Interface

- LVDS Receiver : Tcon (merged)

- Data Format (JEIDA & Normal)

Default LVDS Option: JEIDA

| | | LVDS pin | | JEIDA -DATA | VESA -C | ATA | | |
|-----------------|-----------|------------|-----------------|---------------|---------|------|--|--|
| | | TxIN/RxOU | Т0 | R2 | R0 | | | |
| | | TxIN/RxOU | T1 | R3 | R1 | R1 | | |
| | | TxIN/RxOU | T2 | R4 | R2 | | | |
| Tx | OUT/RxIN0 | TxIN/RxOU | T3 | R5 | R3 | | | |
| | | TxIN/RxOU | T4 | R6 | R4 | | | |
| | | TxIN/RxOU | T6 | R7 | R5 | | | |
| | | TxIN/RxOU | T7 | G2 | G0 | | | |
| | | TxIN/RxOU | T8 | G3 | G1 | | | |
| | | TxIN/RxOU | Т9 | G4 | G2 | | | |
| | | TxIN/RxOUT | 12 | G5 | G3 | | | |
| Tx | OUT/RxIN1 | TxIN/RxOUT | 13 | G6 | G4 | | | |
| | | TxIN/RxOUT | 14 | G7 | G5 | | | |
| | | TxIN/RxOUT | 15 | B2 | В0 | | | |
| | | TxIN/RxOUT | ⁻ 18 | B3 | B1 | | | |
| | | TxIN/RxOUT | 19 | B4 | B2 | | | |
| | | TxIN/RxOUT | ⁻ 20 | B5 | В3 | В3 | | |
| | | TxIN/RxOUT | ⁻ 21 | В6 | В4 | | | |
| Tx | OUT/RxIN2 | TxIN/RxOUT | -22 | В7 | B5 | | | |
| | | TxIN/RxOUT | ⁻ 24 | HSYNC | HSYNC | | | |
| | | TxIN/RxOUT | ⁻ 25 | VSYNC | VSYN | С | | |
| | | TxIN/RxOUT | ⁻ 26 | DEN | DEN | | | |
| | | TxIN/RxOUT | ⁻ 27 | R0 | R6 | | | |
| | | TxIN/RxOU | T5 | R1 | R7 | | | |
| | | TxIN/RxOUT | ⁻ 10 | G0 | G6 | | | |
| Tx | OUT/RxIN3 | TxIN/RxOU1 | 11 | G1 | G7 | | | |
| | | TxIN/RxOUT | 16 | В0 | В6 | | | |
| | | TxIN/RxOUT | 17 | B1 | B7 | | | |
| | | TxIN/RxOU1 | 23 | RESERVED | RESER\ | /ED | | |
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5.5 Input Signals, Basic Display Colors and Gray Scale of Each Color

| | | | | | | | | | | | | D/ | ATA S | SIGNA | ۸L | | | | | | | | | | | GRAY |
|---------------|-------------------|----|----|----|----|----|----|----|----|----|----|----|-------|-------|----|----|----|----|----|----|----|----|----|----|----|-------|
| COLOR | DISPLAY (8bit) | | | | RE | ΞD | | | | | | | GRE | EEN | | | | | | | BL | UE | | | | SCALE |
| | (02.1) | R0 | R1 | R2 | R3 | R4 | R5 | R6 | R7 | G0 | G1 | G2 | G3 | G4 | G5 | G6 | G7 | В0 | B1 | B2 | ВЗ | B4 | B5 | В6 | В7 | LEVEL |
| | BLACK | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | - |
| | BLUE | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 1 | 1 | 1 | 1 | 1 | 1 | 1 | 1 | - |
| | GREEN | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 1 | 1 | 1 | 1 | 1 | 1 | 1 | 1 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | - |
| BASIC | CYAN | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 1 | 1 | 1 | 1 | 1 | 1 | 1 | 1 | 1 | 1 | 1 | 1 | 1 | 1 | 1 | 1 | _ |
| COLOR | RED | 1 | 1 | 1 | 1 | 1 | 1 | 1 | 1 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | - |
| | MAGENTA | 1 | 1 | 1 | 1 | 1 | 1 | 1 | 1 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 1 | 1 | 1 | 1 | 1 | 1 | 1 | 1 | - |
| | YELLOW | 1 | 1 | 1 | 1 | 1 | 1 | 1 | 1 | 1 | 1 | 1 | 1 | 1 | 1 | 1 | 1 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | - |
| | WHITE | 1 | 1 | 1 | 1 | 1 | 1 | 1 | 1 | 1 | 1 | 1 | 1 | 1 | 1 | 1 | 1 | 1 | 1 | 1 | 1 | 1 | 1 | 1 | 1 | _ |
| | BLACK | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | R0 |
| | | 1 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | R1 |
| ODAY | DARK | 0 | 1 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | R2 |
| GRAY SCALE | 1 | : | : | : | : | : | : | | | : | : | : | : | : | : | | | | · | : | : | : | : | | | R3~ |
| OF RED | LIGHT | : | : | : | : | : | : | | | : | : | : | : | : | : | | | · | : | : | : | : | : | | | R252 |
| | | 1 | 0 | 1 | 1 | 1 | 1 | 1 | 1 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | R253 |
| | | 0 | 1 | 1 | 1 | 1 | 1 | 1 | 1 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | R254 |
| | RED | 1 | 1 | 1 | 1 | 1 | 1 | 1 | 1 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | R255 |
| | BLACK | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | G0 |
| | | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 1 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | G1 |
| ODAY | DARK | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 1 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | G2 |
| GRAY SCALE | 1 | : | : | : | : | : | : | | | | | : | : | : | : | | | : | : | : | : | : | : | | | G3~ |
| OF GREEN | \downarrow | : | : | : | : | : | : | | | ; | : | : | : | : | : | | | : | : | : | : | : | : | | | G252 |
| | LIGHT | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 1 | 0 | 1 | 1 | 1 | 1 | 1 | 1 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | G253 |
| | | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 1 | 1 | 1 | 1 | 1 | 1 | 1 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | G254 |
| | GREEN | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 1 | 1 | 1 | 1 | 1 | 1 | 1 | 1 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | G255 |
| | BLACK | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | В0 |
| | | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 1 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | В1 |
| ODAY | DARK | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 1 | 0 | 0 | 0 | 0 | 0 | 0 | B2 |
| GRAY SCALE | 1 | | | | : | : | : | | | : | : | : | : | : | : | | | : | : | : | : | : | : | | | B3~ |
| OF BLUE | 1 | : | | : | : | : | : | | | : | : | : | : | : | : | | | : | : | : | : | : | : | | | B252 |
| | LIGHT | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 1 | 0 | 1 | 1 | 1 | 1 | 1 | 1 | B253 |
| | | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 1 | 1 | 1 | 1 | 1 | 1 | 1 | B254 |
| | BLUE | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 1 | 1 | 1 | 1 | 1 | 1 | 1 | 1 | B255 |

Note) Definition of Gray:

Rn: Red Gray, Gn: Green Gray, Bn: Blue Gray (n = Gray level)

Input Signal: 0 = Low level voltage, 1 = High level voltage

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6. Interface Timing

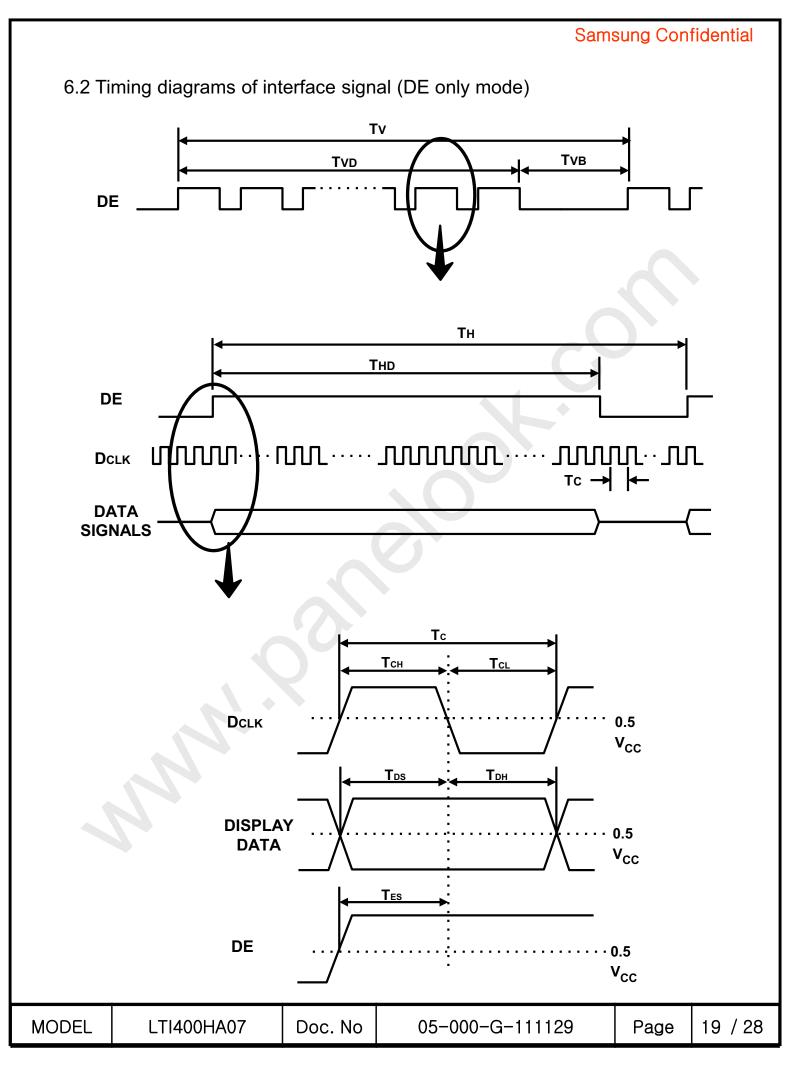
6.1 Timing Parameters (DE only mode)

| Signal | Item | Symbol | Min. | Тур. | Max. | Unit | Note |
|--------------|-----------------------------|------------------|-------|-------|-------|--------|------|
| Clock | | 1/T _C | 130.0 | 148.5 | 155.0 | MHz | - |
| Hsync | Frequency | F _H | 50.0 | 67.5 | 75.0 | KHz | - |
| Vsync | | F_{V} | 48 | 60 | 62 | Hz | - |
| Vertical | Active Display Period | T _{VD} | - | 1080 | - | Lines | - |
| Display Term | Vertical Total | T _V | 1100 | 1125 | 1158 | Lines | - |
| Horizontal | Active Display Period | T _{HD} | - | 1920 | - | Clocks | - |
| Display Term | Horizontal Total | T _H | 2090 | 2200 | 2350 | Clocks | - |

Note) Note) Requirement: ODD channel → Vsync

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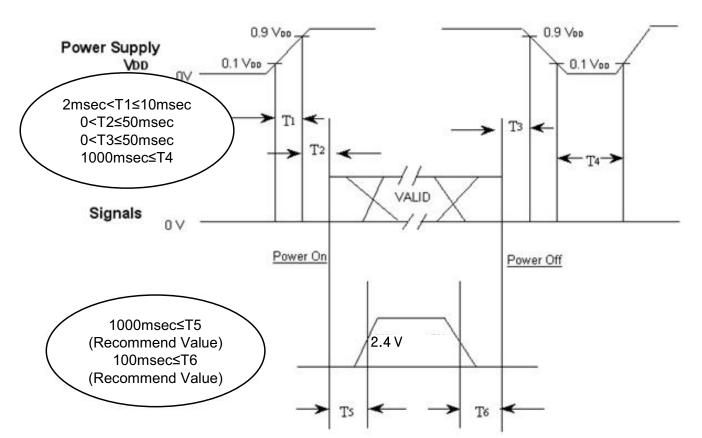


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6.3 Power ON/OFF Sequence

To prevent a latch-up or DC operation of the LCD Module, the power on/off sequence should be as the diagram below.



T1 : V_{DD} rising time from 10% to 90% $\,$

T2: The time from V_{DD} to valid data at power ON.

T3 : The time from valid data off to V_{DD} off at power Off.

T4: V_{DD} off time for Windows restart

T5: The time from valid data to B/L enable at power ON.

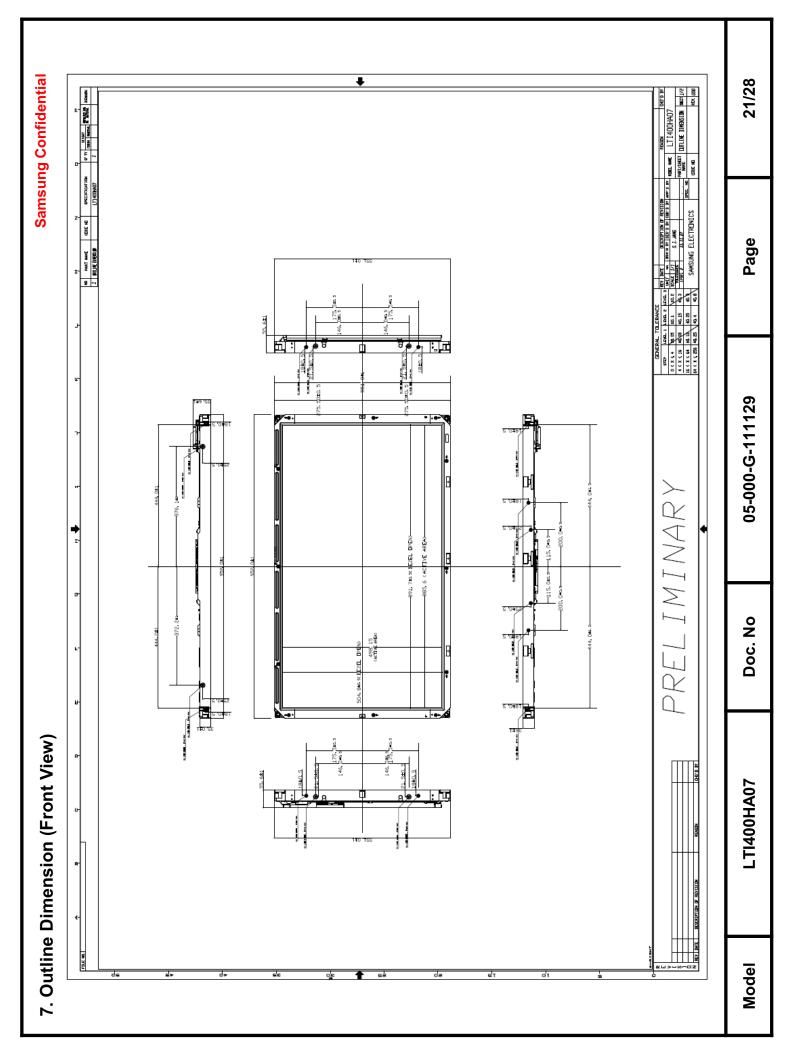
T6: The time from valid data off to B/L disable at power Off.

- The supply voltage of the external system for the Module input should be the same as the definition of V_{DD}.
- Apply the lamp voltage within the LCD operation range. When the back light turns on before the LCD operation or the LCD turns off before the back light turns off, the display may momentarily show abnormal screen.
- In case of V_{DD} = off level, please keep the level of input signals low or keep a high impedance.
- T4 should be measured after the Module has been fully discharged between power off and on period.
- Interface signal should not be kept at high impedance when the power is on.

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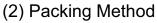


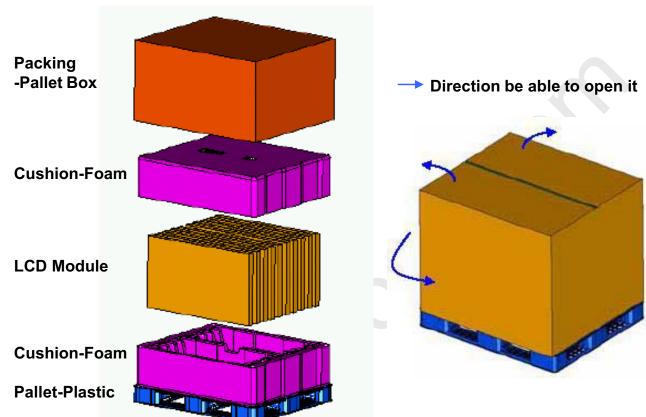


8. PACKING

8.1 CARTON (Internal Package)

(1) Packing Form Corrugated fiberboard box and corrugated cardboard as shock absorber





8.2 Packing Specification

| Item | Specification | Remark |
|---------------------|---------------------------------|--|
| LCD Packing | 10ea / (Packing- Pallet Box) | 1. 100 Kg / LCD (10ea) 2. 7 Kg / Cushion-pallet (2ea) 3. 6.7 Kg / Packing-Pallet Box (1ea) 4. Cushion-pallet Material : EPS 5. Packing-Pallet Box Material : DW4 |
| Pallet | 1Box / Pallet | 1. Pallet weight = 8kg |
| Packing Direction | Vertical | |
| Total Pallet Size | H x V x height | 1150mm(H) x 985mm(V) x 609mm(height) |
| Total Pallet Weight | 121.7 kg | Pallet(8kg) + Module (10.0*10=100kg) + Cushion (up + bottom=7kg) + Pallet-BOX(6.7kg) |

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8.3 Packing Storage condition

| ITEM | Unit | Min. | Max. | | |
|---------------------|-----------|------|------|--|--|
| Storage Temperature | (℃) | 5 | 40 | | |
| Storage Humidity | (%rH) | 35 | 75 | | |
| Storage life | 12 months | | | | |
| Storage Condition | | | | | |

8.4 Packing long-term Storage guide

| Long –term Storage Process | More than 3months Storage or Low temp. Delivery/under 5° Storage, \rightarrow On the 20 $^{\circ}$ 50%rH Condition , More than 10hrs release. |
|-------------------------------|--|
|-------------------------------|--|

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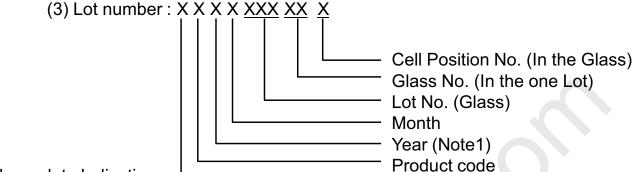
9. MARKING & OTHERS

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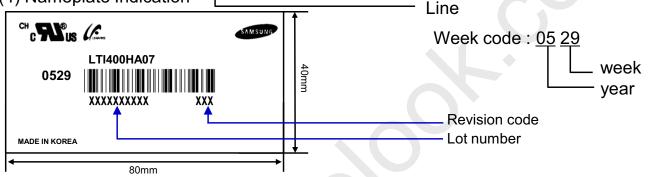
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A nameplate bearing followed by is affixed to a shipped product at the specified location on each product.

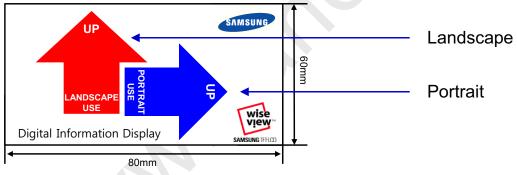
- (1) Part number: LTI400HA07
- (2) Revision: Three letters



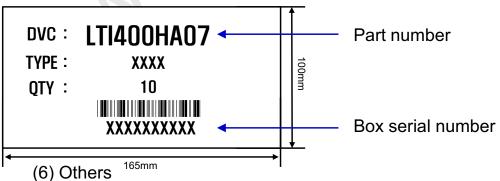
(4) Nameplate Indication



(5) Landscape / Portrait Direction Indication



(6) Packing box attach



1. After service part

Lamps cannot be replaced because of the narrow bezel structure.

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10. General Precautions

10.1 Handling

- (a) When the Module is assembled, it should be attached to the system firmly using all mounting holes. Be careful not to twist and bend the Module.
- (b) Because the inverter use high voltage, it should be disconnected from power before it is assembled or disassembled.
- (c) Refrain from strong mechanical shock and / or any force to the Module. In addition to damage, this may cause improper operation or damage to the Module and CCFT back light.
- (d) Note that polarizers are very fragile and could be damage easily. Do not press or scratch the surface harder than a HB pencil lead.
- (e) Wipe off water droplets or oil immediately. If you leave the droplets for a long time, staining or discoloration may occur.
- (f) If the surface of the polarizer is dirty, clean it using absorbent cotton or soft cloth.
- (g) Desirable cleaners are water, IPA(Isopropyl Alcohol) or Hexane. Do not use Ketone type materials(ex. Acetone), Ethyl alcohol, Toluene, Ethyl acid or Methyl chloride. It might permanent damage to the polarizer due to chemical reaction.
- (h) If the liquid crystal material leaks from the panel, it should be kept away from the eyes or mouth. In case of contact with hands, legs or clothes, it must be washed away with soap thoroughly.
- (i) Protect the Module from static, or the CMOS Gate Array IC would be damaged.
- (j) Use finger-stalls with soft gloves in order to keep display clean during the incoming inspection and assembly process.
- (k) Do not disassemble the Module.
- (I) Do not pull or fold the lamp wire.
- (m) Do not adjust the variable resistor located on the Module.
- (n) Protection film for polarizer on the Module should be slowly peeled off just before use so that the electrostatic charge can be minimized.
- (o) Pins of I/F connector should not be touched directly with bare hands.

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10.2 Storage

- (a) Do not leave the Module in high temperature, and high humidity for a long time. It is highly recommended to store the Module with temperature from 0 to 35 $^{\circ}$ C and relative humidity of less than 70%.
- (b) Do not store the TFT-LCD Module in direct sunlight.
- (c) The Module should be stored in a dark place. It is prohibited to apply sunlight or fluorescent light in storing.

10.3 Operation

- (a) Do not connect or disconnect the Module in the "Power On" condition.
- (b) Power supply should always be turned on/off by the "Power on/off sequence"
- (c) Module has high frequency circuits. Sufficient suppression to the electromagnetic interference should be done by system manufacturers.
 Grounding and shielding methods may be important to minimize the interference.
- (d) The cable between the back light connector and its inverter power supply should be connected directly with a minimized length. A longer cable between the back light and the inverter may cause lower luminance of lamp(CCFT) and may require higher startup voltage(Vs).

10.4 Operation Condition Guide

- (a) The LCD product should be operated under normal conditions. Normal condition is defined as below;
 - Temperature : 20±15℃
 - Humidity : $55\!\pm\!20\%$
 - Display pattern : continually changing pattern (Not stationary)
- (b) If the product will be used in extreme conditions such as high temperature, humidity, display patterns or operation time etc.., It is strongly recommended to contact SEC for Application engineering advice. Otherwise, its reliability and function may not be guaranteed. Extreme conditions are commonly found at Airports, Transit Stations, Banks, Stock market, and Controlling systems.

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10.5 Others

- (a) Ultra-violet ray filter is necessary for outdoor operation.
- (b) Module should be turned clockwise (regular front view perspective) when used in portrait mode
- (c) Avoid condensation of water. It may result in improper operation or disconnection of electrode.
- (d) Do not exceed the absolute maximum rating value. (supply voltage variation, input voltage variation, variation in part contents and environmental temperature, and so on)
 Otherwise the Module may be damaged.
- (e) If the Module keeps displaying the same pattern for a long period of time, the image may be "sticked" to the screen.To avoid image sticking, it is recommended to use a screen saver.
- (f) This Module has its circuitry PCB's on the rear side and should be handled carefully in order not to be stressed.
- (g) Please contact SEC in advance when you display the same pattern for a long time.

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